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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
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IBM CORPORATION ROCHESTER IP LAW DEPT. 917 3605 HIGHWAY 52 NORTH ROCHESTER, MN 55901-7829			EXAMINER THOMPSON, JR, OTIS L	
			ART UNIT 4183	PAPER NUMBER
			MAIL DATE 11/27/2007	DELIVERY MODE PAPER

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Office Action Summary

Application No.

10/733,754

Applicant(s)

KIEL ET AL.

Examiner

Otis L. Thompson, Jr.

Art Unit

4183

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 11 December 2003.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-44 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-44 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
- Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
- Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
- ☐ Certified copies of the priority documents have been received.
 - ☐ Certified copies of the priority documents have been received in Application No. _____.
 - ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- ☒ Notice of References Cited (PTO-892)
- ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- ☒ Information Disclosure Statement(s) (PTO/SB/08)
Paper No(s)/Mail Date 12/11/2003.
- ☐ Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____.
- ☐ Notice of Informal Patent Application
- ☐ Other: _____.

DETAILED ACTION

Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

2. Claims 1, 3-5, and 43-44 are rejected under 35 U.S.C. 102(b) as being anticipated by admitted prior art Gioquindo et al. (U.S. 2002/0029286 A1).
3. As to claims 1 and 43, Gioquindo et al. discloses a method for communication between multiple partitions that share a communications adapter. In figure 6 an example method of the communication is provided. In step 200, LPAR 3 constructs an IP datagram to send to 9.117.34.254 (corresponding IP address for LPAR 1). In step 210 the IP datagram is passed to the channel driver and no LAN information is needed in the packet. In step 220 the packet is transmitted across the channel to the OSA (Open Systems Adapter). In step 230 the OSA uses the destination IP address 9.117.34.254 to look up the entry in the Address Resolution Protocol (ARP) cache. The entry is found in step 240, and the IP datagram is forwarded to LPAR 1 without ever being transmitted onto the LAN. The OSA is capable of communicating with different media types such as Fiber Distributed Data Interface, token ring, or Ethernet (Column 6, paragraph 32).

As to claim 3 and 44, Gioquindo et al. discloses step 230 of the method shown in

figure 6. In this step, the destination is determined by looking up the 9.117.34.254 IP address in an ARP cache. In step 240 the entry in the ARP cache is found to be the corresponding IP address for LPAR 1.

As to claim 4, Gioquindo et al. discloses, in step 230 of the method shown in figure 6, that an IP address stored in the ARP cache is used to determine the destination partition for a packet.

As to claim 5, Gioquindo et al. discloses step 220 in which the packet is transmitted across the channel to the OSA. Gioquindo et al. further discloses step 230 of figure 6 in which an ARP cache (table) stored in the OSA is accessed to look up the 9.117.34.254 IP address. In the ARP cache, provision is made for partition-to-partition communication of IP datagrams by storing IP addresses of the LPARS as home addresses (Abstract).

Claim Rejections - 35 USC § 103

4. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

5. Claims 2 and 36 are rejected under 35 U.S.C. 103(a) as being unpatentable over admitted prior art Gioquindo et al. (U.S. 2002/0029286 A1) as applied to claim 1 above, and further in view of Duncan et al. (U.S. 2003/0149844 A1).

6. Gioquindo et al. discloses the claimed invention above. Gioquindo et al. does not specifically disclose the generation of an interrupt and a message signaling interrupt (MSI) to notify each of the one or more partitions to which a frame or packet is transferred of the frame or packet.

However, Duncan et al. discloses a method in which a source partition is able to generate an MSI using its interrupt engine. The interrupt engine passes the MSI to the interrupt port of the receiving partition (Column 10, paragraph 46). This interrupt serves the purpose of coherently moving information between source and destination partitions.

Therefore, it would have been obvious to one of ordinary skill in the art at the time the applicants' invention was made to incorporate the teachings of Duncan et al. into Gioquindo et al. by using an interrupt engine to generate MSI messages in order to coherently move information between source and destination partitions.

Claim Rejections - 35 USC § 103

7. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

8. Claims 6-9, 22-25, 37, and 40-42 are rejected under 35 U.S.C. 103(a) as being unpatentable over Fernandes et al. (U.S. 2003/0236852) in view of admitted prior art

Gioquindo et al. (U.S. 2002/0029286 A1), and further in view of admitted prior art Condon (U.S. 5,956,714).

9. Fernandes et al. discloses a data processing system comprising a plurality of processors (Figure 1, labels 102a and 102b), a system memory (Figure 1, label 106), multiple logical partitions (Figure 1, labels 101a and 101b), and a network adapter being shared by multiple partitions and coupled to the aforementioned components (Figure 2, label 225) that transmits packets to one or more of the logical partitions.

Fernandes et al. further discloses the forwarding of a broadcast packet to each registered LPAR. Referring to figure 3, Fernandes et al. discloses that all data packets traversing network 230 will be received and accepted by adapter 225. NIC driver 310 then performs software filtering and routing of each packet based upon MAC address. If the MAC address indicates that the corresponding packet is a broadcast packet, NIC driver 310 will forward the packet to each registered LPAR typically via the inter-LPAR communication facility (Column 5, paragraph 26).

Fernandes et al. does not specifically disclose the polling of a transmit queue in order to determine if a partition has a frame or packet to transmit, the accessing of a table stored in the adapter to select a partition to which a frame or packet is to be sent, and the selection of a frame or packet based on a priority value stored in the table.

However, Gioquindo et al discloses an ARP (address resolution protocol) cache (table) stored in an OSA (open systems adapter) that is designed to store full MAC address headers and IP addresses of logical partitions for the purpose of partition-to-

partition communication of IP datagrams (Abstract; Column 7, paragraph 46); and Condon discloses a queuing system using a relational database which serves the purpose of manipulating items sent between a plurality of application servers. In figure 3, Condon describes a process by which the server selects items from the queue using the logical partition number. At step 20, a logical partition number is selected. Each logical partition number has a plurality of rows associated with the logical partition number. At step 22 access to the rows associated with the selected partition number is locked. The server then monitors all rows associated with the logical partition number at step 24 to determine if any items are ripe for dequeuing, taking into account the type of queue being monitored. If a ripe item is found at step 25, the server dequeues the ripe item at step 26 and forwards it to a proper application server at step 28 (Column 5, lines 32-45). Condon also discloses that various types of queues may be implemented, for example time based, priority based, and FIFO based queues. Priority based queues give certain items a higher priority with respect to other items on the queue. Items having a high priority are dequeued before items at the front of the queue (Column 4, lines 40-42 and 50-53).

Therefore, it would have been obvious to one of ordinary skill in the art at the time the applicants' invention was made to combine the teachings of Condon with the teachings of Fernandes et al. and Gioquindo et al. in order to enable the communication of IP datagrams between partitions and in order to manipulate items sent between a plurality of application servers.

Claim Rejections - 35 USC § 103

10. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

11. Claims 10, 11, 26, and 27 are rejected under 35 U.S.C. 103(a) as being unpatentable over Fernandes et al. (U.S. 2003/0236852) in view of admitted prior art Gioquindo et al. (U.S. 2002/0029286 A1), and further in view of Schmidt et al. (6,854,021 B1).

12. Fernandes et al. discloses a data processing system comprising a plurality of processors (Figure 1, labels 102a and 102b), a system memory (Figure 1, label 106), multiple logical partitions (Figure 1, labels 101a and 101b), and a network adapter being shared by multiple partitions and coupled to the aforementioned components (Figure 2, label 225) that transmits packets to one or more of the logical partitions.

Fernandes et al. does not specifically disclose that the network adapter includes a table, that each partition employs a receive queue and a transmit queue, that the table stored on the network adapter is accessed in order to determine where data is to be sent, and that at least one transmit queue of one of the partitions transfers data to the network adapter and the network adapter transmits the data to the receive queue of one the partitions based on data stored in the table.

However, Gioquindo et al discloses an ARP (address resolution protocol) cache (table) stored in an OSA (open systems adapter) that is designed to store full MAC address headers and IP addresses of logical partitions for the purpose of partition-to-partition communication of IP datagrams (Abstract; Column 7, paragraph 46); and Schmidt et al. discloses a method and apparatus for sending data from one partition to a second partition within a logically partitioned computer. Referring to figure 2, Schmidt et al. discloses a schematic diagram of a single computer divided up into a number of logical partitions 212a-212n, each partition having discrete servers 214a-214n respectively. Each discrete server has a TCP/IP layer 216a-216n respectively, and below that a device driver 218a-218n respectively for the purpose of driving data transmissions between discrete servers. Each device driver has a send queue 222a-222n respectively, and a receive queue 220a-220n respectively. A common lookup table 224 is a centralized table defining the discrete servers 214a-214n within the computer 210 (Column 4, lines 24-39).

Schmidt et al. further discloses that the send queue and receive queue are registered in the lookup table, which is visible to all partitions. A send instruction interrogates the lookup table to locate the send queue and the receive queue, and sends the data in the send queue in the first logical partition to the receive queue in the second logical partition. The method and apparatus disclosed in Schmidt et al. serves the purpose of allowing data to be transferred between discrete servers in logical partitions while maintaining security between the logical partitions (Abstract).

Therefore, it would have been obvious to one of ordinary skill in the art at the time the applicants' invention was made to combine the teachings of Schmidt et al. with the teachings of Fernandes et al. and Gioquindo et al. in order to enable partition-to-partition communication of data and to maintain security between partitions while doing so.

Claim Rejections - 35 USC § 103

13. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

14. Claims 12-16 and 28-34 are rejected under 35 U.S.C. 103(a) as being unpatentable over Fernandes et al. (U.S. 2003/0236852) in view of admitted prior art Gioquindo et al. (U.S. 2002/0029286 A1), and further in view of McMichael et al. (U.S. 2003/0023826 A1).

15. Fernandes et al. discloses the following elements of the rejected claims:

A data processing system comprising a plurality of processors (Figure 1, labels 102a and 102b), a system memory (Figure 1, label 106), multiple logical partitions (Figure 1, labels 101a and 101b), and a network adapter being shared by multiple partitions and coupled to the aforementioned components (Figure 2, label 225).

Fernandes et al. further discloses that the data processing system allows the other partitions to share the network adapter by means of registration with the host partition (Abstract).

Fernandes et al. further discloses a method for allowing a partition to share the network adapter. Referring to figure 3, Fernandes et al. discloses that a primary partition initially takes control of the resource to be shared (LPAR1 assumes ownership and control of network adapter 225 via driver code identified as NIC driver 310). NIC driver 310 is contained inside the host partition LPAR1. After taking control of the network adapter, the owning partition configures the resource to recognize and accept all traffic on the network. Each logical partition that wants to share the network adapter 225 with LPAR1 is required to send a unique identifier (MAC address or a set of low level network identifiers) to LPAR1 and NIC driver 310 in order to register itself for communication (Column 4, paragraphs 21-22; Column 5, paragraphs 23-25). Once the NIC driver 310 and LPAR1 have received the unique identifier (MAC address), hardware filtering of packets is deactivated on the registered partition and then maintained by the host partition LPAR1. The NIC driver 310 maintains a list of registered MAC addresses and forwards a packet to a partition if a match is detected (Abstract).

Fernandes et al. does not specifically disclose a partition management tool, the creation of a new partition in the new computer system, and a table stored in the adapter.

However, Gioquindo et al. discloses an ARP (address resolution protocol) cache (table) stored in an OSA (open systems adapter) that is designed to store full MAC address headers and IP addresses of logical partitions for the purpose of partition-to-partition communication of IP datagrams (Abstract; Column 7, paragraph 46); and McMichael et al. discloses a partition manager that enables the dynamic creation, reconfiguration, and deletion of logical volumes as non-removable storage devices are connected, reconfigured, or disconnected in a computer system by assigning partitions on a connected device to volume managers which create the logical volumes, and by removing the assignment when the device is disconnected or reconfigured (Abstract). This partition manager fulfills the need for an operating system that supports dynamic changes in the characteristics of physical devices underlying logical volumes without requiring that the higher layers of the operating system and user applications modify the addresses of the logical volumes (Column 2, paragraph 10).

Therefore, it would have been obvious to one of ordinary skill in the art at the time the applicants' invention was made to combine the teachings of McMichael et al. with Fernandes et al. and Gioquindo et al. in order to store the IP or MAC addresses of logical partitions and to allow for dynamic changes in physical devices without requiring that the operating system and user applications modify the addresses of logical volumes.

Claim Rejections - 35 USC § 103

16. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

17. Claim 35 is rejected under 35 U.S.C. 103(a) as being unpatentable over admitted prior art Gioquindo et al. (U.S. 2002/0029286 A1) as applied to claim 1 above, and further in view of Fernandes et al. (U.S. 2003/0236852 A1).

18. Gioquindo et al. discloses the claimed invention above. Gioquindo et al. does not specifically disclose the detection of a broadcast frame or packet and transferring the broadcast frame or packet from the adapter to all of the partitions.

However, Fernandes et al. discloses the following method for the purpose of forwarding a broadcast packet to each registered LPAR. Referring to figure 3, Fernandes et al. discloses that all data packets traversing network 230 will be received and accepted by adapter 225. NIC driver 310 then performs software filtering and routing of each packet based upon MAC address. If the MAC address indicates that the corresponding packet is a broadcast packet, NIC driver 310 will forward the packet to each registered LPAR typically via the inter-LPAR communication facility (Column 5, paragraph 26).

Therefore, it would have been obvious to one of ordinary skill in the art at the time the applicants' invention was made to incorporate the teachings of Fernandes et

al. into Gioquindo et al. by using a NIC driver coupled with a network adapter in order to forward broadcast packets to each registered LPAR.

Claim Rejections - 35 USC § 103

19. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

20. Claims 17 and 19-21 are rejected under 35 U.S.C. 103(a) as being unpatentable over admitted prior art Kubala (U.S. 5,564,040) in view of admitted prior art Gioquindo et al. (U.S. 2002/0029286 A1).

21. Kubala discloses an apparatus for providing a server function in a logically partitioned hardware machine comprising a central processing complex that contains one or more central processors (Figure 1, label 103) and main memory for system 100 pictured in figure 1. Kubala further discloses logical partitions 107-109 in figure 1, each of which functioning as a separate hardware machine and having its own operating system (Column 2, lines 65-67; Column 3, lines 1-12). Kubala does not specifically disclose a multiple queue Ethernet adapter coupled to the processors, main memory, and partitions including cache memory, a register, and a table wherein the adapter receives a frame or packet, determines whether the frame or packet is for one of the partitions, stores the frame or packet in cache memory, determines the destination

partition, and transfers the frame or packet to the destination partition. Kubala also does not specifically disclose that the apparatus accesses a table stored on the adapter to determine the partition where a frame or packet is to be sent, that sending is based on at least one of a MAC address, VLAN ID/MAC address pair, or an IP address stored in the table, and that accessing a value stored in a register indicating the type of address is used to determine, from data stored in the table, to which partition a packet of frame should be sent.

However, Gioquindo et al. discloses, in figure 6, an example method for the purpose of communication and data processing between partitions by sharing an adapter. In step 200, LPAR 3 constructs an IP datagram to send to 9.117.34.254 (corresponding IP address for LPAR 1). In step 210 the IP datagram is passed to the channel driver and no LAN information is needed in the packet. In step 220 the packet is transmitted across the channel to the OSA (Open Systems Adapter). In step 230 the OSA uses the destination IP address 9.117.34.254 to look up the entry in the Address Resolution Protocol (ARP) cache. The entry is found in step 240, and the IP datagram is forwarded to LPAR 1 without ever being transmitted onto the LAN. The OSA is capable of communicating with different media types such as Fiber Distributed Data Interface, token ring, or Ethernet (Column 6, paragraph 32).

Gioquindo et al. further discloses step 230 of the method shown in figure 6, which serves the purpose of determining the destination partition. In this step, the destination is determined by looking up the 9.117.34.254 IP address in an ARP cache.

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In step 240 the entry in the ARP cache is found to be the corresponding IP address for LPAR 1.

Gioquindo et al. further discloses, in step 230 of the method shown in figure 6, that an IP address stored in the ARP cache is used to determine the destination partition for a packet. The purpose of the IP address is to identify each individual partition.

Gioquindo et al. further discloses step 220 in which the packet is transmitted across the channel to the OSA. In step 230 of figure 6 an ARP cache (table) stored in the OSA and is accessed to look up the 9.117.34.254 IP address. In the ARP cache, provision is made for partition-to-partition communication of IP datagrams by storing IP addresses of the LPARS as home addresses (Abstract). The purpose of the ARP cache is to house addresses of the logical partitions so that the destination partition can be determined.

Therefore, it would have been obvious to one of ordinary skill in the art at the time the applicants' invention was made to incorporate the teachings of Gioquindo et al. into Kubala in order to determine the destination partition for a packet, to identify each individual partition, to transmit the packet to the determined destination partition, and to communicate and share data between partitions using a shared adapter.

Claim Rejections - 35 USC § 103

22. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

23. Claims 18 and 39 are rejected under 35 U.S.C. 103(a) as being unpatentable over admitted prior art Kubala (U.S. 5,564,040) in view of admitted prior art Gioquindo et al. (U.S. 2002/0029286 A1) as applied to claim 17 above, and further in view of Duncan et al. (U.S. 2003/0149844 A1).

24. Kubala discloses the claimed invention above but lacks the teachings of generating an interrupt and generating a Message Signaling Interrupt (MSI) to notify a partition of transferred packet.

However, Duncan et al. discloses a method in which a source partition is able to generate an MSI using its interrupt engine. The interrupt engine passes the MSI to the interrupt port of the receiving partition (Column 10, paragraph 46). This interrupt serves the purpose of coherently moving information between source and destination partitions.

Therefore, it would have been obvious to one of ordinary skill in the art at the time the applicants' invention was made to incorporate the teachings of Duncan et al. into Gioquindo et al. by using an interrupt engine to generate MSI messages in order to coherently move information between source and destination partitions.

Claim Rejections - 35 USC § 103

25. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

26. Claim 38 is rejected under 35 U.S.C. 103(a) as being unpatentable over admitted prior art Kubala (U.S. 5,564,040) in view of admitted prior art Gioquindo et al. (U.S. 2002/0029286 A1) as applied to claim 17 above, and further in view of Fernandes et al. (U.S. 2003/0236852 A1).

27. Kubala discloses the claimed invention above but lacks the teaching of determining if a frame or packet is a broadcast frame or packet and transmitting the broadcast frame or packet to all of the partitions that share the adapter.

However, Fernandes et al. discloses the following method for the purpose of forwarding a broadcast packet to each registered LPAR. Referring to figure 3, Fernandes et al. discloses that all data packets traversing network 230 will be received and accepted by adapter 225. NIC driver 310 then performs software filtering and routing of each packet based upon MAC address. If the MAC address indicates that the corresponding packet is a broadcast packet, NIC driver 310 will forward the packet to each registered LPAR typically via the inter-LPAR communication facility (Column 5, paragraph 26).

Therefore, it would have been obvious to one of ordinary skill in the art at the time the applicants' invention was made to incorporate the teachings of Fernandes et al. into Gioquindo et al. by using a NIC driver coupled with a network adapter in order to forward broadcast packets to each registered LPAR.

Conclusion

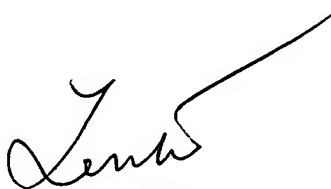
Any inquiry concerning this communication or earlier communications from the examiner should be directed to Otis L. Thompson, Jr. whose telephone number is (571)270-1953. The examiner can normally be reached on Monday to Thursday 7:30 am to 5:00 pm EST.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Len Tran can be reached on (571)272-1184. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/Otis L Thompson, Jr./
Examiner, Art Unit 4183

November 16, 2007


LEN TRAN
PRIMARY EXAMINER
11/13/07
Supervisory